



ANURAG GROUP OF INSTITUTIONS
(FORMERLY CVSR COLLEGE OF ENGINEERING)
Venkatapur (V), Ghatkesar (M), R.R.Dist.

ACTIVITY REPORT

Name of the activity: **Guest lecture**

Title /Topic: **“Analog and Mixed Signal IC Design”**

Resource Person: **Mr.V.Veeresh Babu**, Analog Designer, AMS Semiconductors

India Pvt. Ltd.

Date: 29-12-2017

Key points: VLSI Designing part.

Summary: One day guest lecture on basics of Analog signal and mixed signal designing with various circuits and brief explanation of demo kits using VLSI platform. M.Tech (VLSI), IV year ECE and III year ECE 60 students participated in E-Block Auditorium. The faculty coordinators are Mrs.T.Annamani and Mr.E.Srinivas.

Photographs:



Report prepared by: Mrs.T.Annamani