

# Activity Report

**Name of Activity:** Two Days Workshop

**Title/ Topic:** “ CADENCE” in Association with IETE student chapter

**Resource Person :** Dr. Harishanker Srivastava

**Date:** 30-07-2018 to 31-07-2018

## Key Point Highlights

### 1. 1<sup>st</sup> -Day (30-07-2018)-Session-I:

Dr. Harishanker Srivastav had explained the importance of Cadence tool. He also explained that Cadence offers a broad portfolio of tools to address an array of challenges related to custom IC, digital, IC package, and PCB design and system-level verification.

### 2. 2<sup>nd</sup> Day (16-11-2017)-Session-II:

He explained design and simulation of the digital circuit like adders. Students designed adders using various techniques.

## Summary

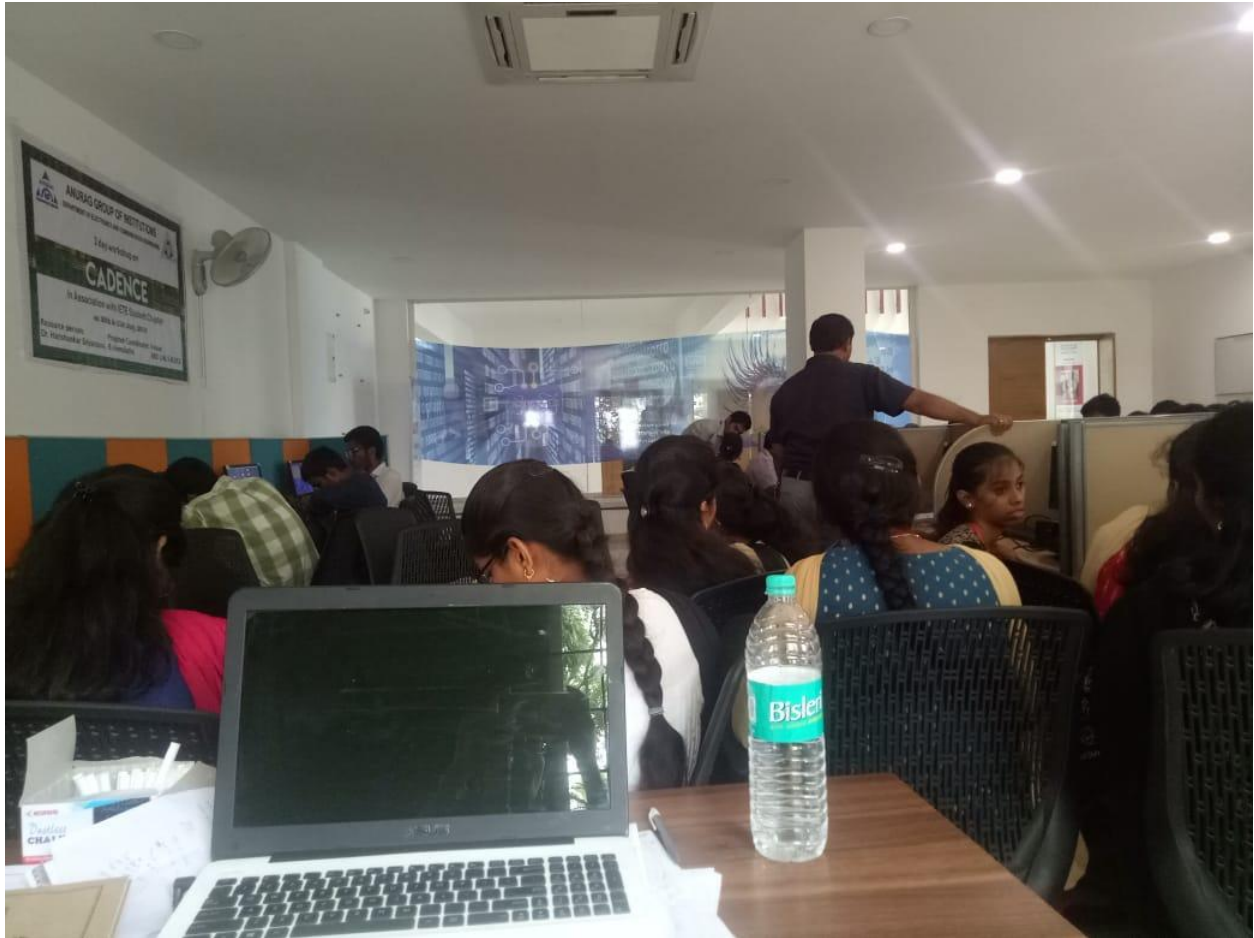
The Department of Electronics and Communication Engineering organised a 2-day workshop on “CADENCE”, from 30/07/2018 to 31/07/2018, for IV Year students by Dr. Hari Shanker Srivastava, Professor, Dept of Electronics & Communication and Engineering, Anurag Group of Institutions, under IETE student chapter. 44 students attended the workshop. Sir explained that Cadence offers a broad portfolio of tools to address an array of challenges related to custom IC, digital, IC package, and PCB design and system-level verification. He also explained the students about the importance of Cadence in designing and simulating the digital circuit. Students designed Full Adder circuits using various methods.

**Photographs**









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